Dynamic Compilation Everywhere DCE 2013

Second Workshop : **Dynamic compilation from SoC to Web Browser via HPC**
https://sites.google.com/site/dynamiccompilationeverywhere/

Held in conjunction with the 8th HiPEAC Conference http://www.hipeac.net/conference/

January 22, 2013, Berlin, Germany

Topics : General purpose as well as integrated processors nowadays have to run programs written in a wide variety of languages with isolation concerns. Dynamic compilation, i.e. generate binary code at run-time, is becoming a viable solution for many usage scenarios, and the goal of this workshop is to present current research and look forward to what is going to happen in this field of growing interest for the coming years.

Keywords : Performance, power constraints, time constraints, thermal constraints, improved reliability, security, IP protection, cross-layer optimization, virtualization, high performance computing, JAVA, Javascript, DBT, emulation, etc.

Objectives :

- **Gather multiple communities :** where dynamic compilation are in use
- **Show the wide usage range** : pick a keyword in the list & show how you use dynamic compilation in this context

Articles will be selected on

Scientific interest but also tutorial nature : we want to reach nearby communities and create interest in dynamic compilation

Dynamic compilation usage : we want to cover the largest possible spectrum of dynamic compilation and optimization uses

Articles Format and Publication The article should be in the same format than the HiPEAC conference : The recommended paper length is 6 to 10 pages using the 7x10 ACM Submission format . The pages includes text, figures and references : http://www.hipeac.net/conference Paper should be uploaded on the easychair web site: https://www.easychair.org/conferences/?conf=dce2013

Important dates :

Paper due :	Acceptance	Camera ready
October 29 2012;	November 26, 2012	December 12, 2012

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